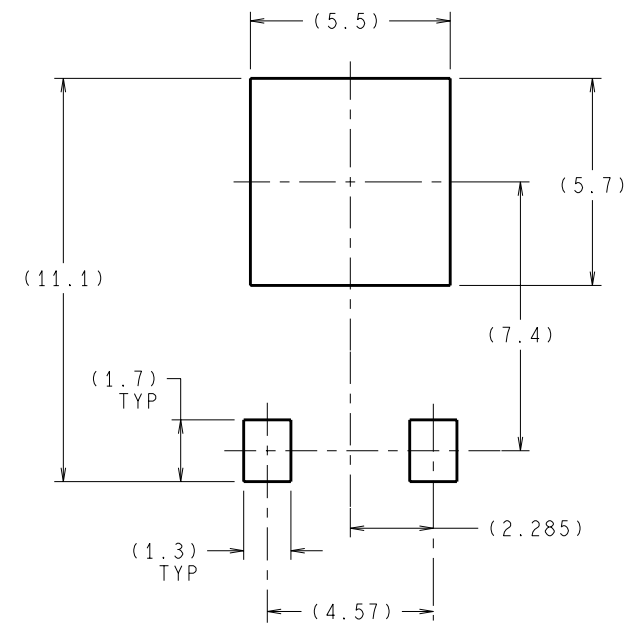
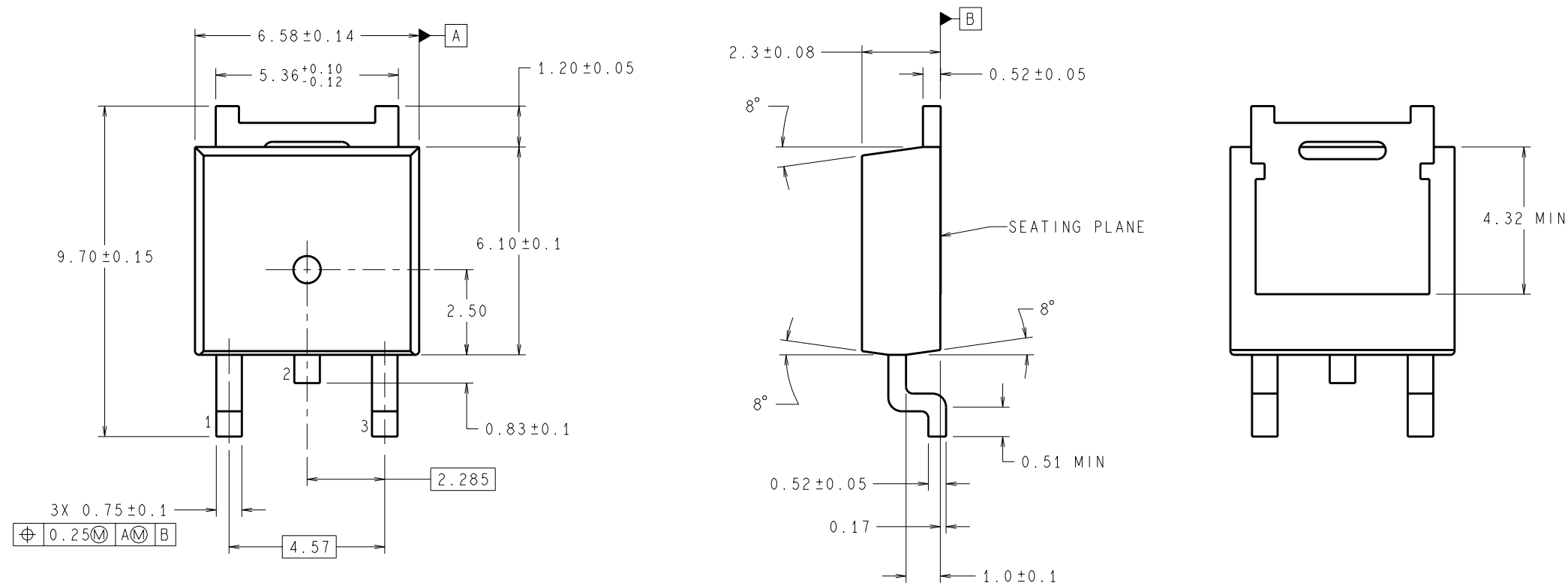


REVISIONS				
LTR	DESCRIPTION	E.C.N.	DATE	BY/APP'D
A	RELEASE TO DOCUMENT CONTROL	12120	10/23/1998	NOR/MS/SH
B	ADD LAND PATTERN RECOMMENDATION VIEW; DIM 2.285 WAS 2.28	12261	06/30/1999	MS/PS



LAND PATTERN RECOMMENDATION



NOTES: UNLESS OTHERWISE SPECIFIED

- STANDARD LEAD FINISH:
5.08 MICROMETERS MINIMUM
LEAD/TIN 15/85 ON TAMAC 4.
- REFERENCE JEDEC REGISTRATION TO-252.
VARIATION AA, ISSUE C, DATED SEPT 1988.

DIMENSIONS ARE IN MILLIMETERS

APPROVALS	DATE	National Semiconductor 2900 Semiconductor dr., Santa Clara, CA 95052-8090 TO-252, MOLDED, JEDEC, 6.58 X 6.10 X 2.30mm BODY, 3 LD, 2.28mm PITCH, S/M			
DRAWN NOR A.R.	10/23/1998				
DFTG. CHK. THANH LEQUANG	06/30/1999				
ENGR. CHK. PETER SPALDING	06/30/1999	SCALE N/A	SIZE C	DRAWING NUMBER (SC)MKT-TD03B	REV B
		DO NOT SCALE DRAWING		SHEET 1 of 1	